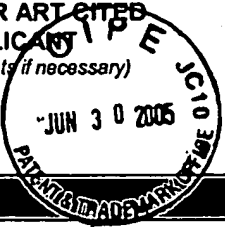
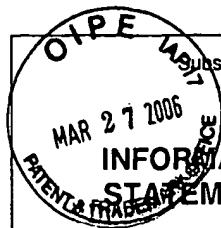


<b>Form PTO-1449</b>  <b>LIST OF PRIOR ART CITED BY APPLICANT</b> <i>(Use several sheets if necessary)</i>			ATTORNEY DOCKET NO. <b>AUS920040064US1</b>		SERIAL NO. <b>10/806,871</b>	
			APPLICANT <b>Dimpsey et al.</b>			
			FILING DATE <b>March 22, 2004</b>		GROUP ART UNIT <b><del>2405</del> 2185</b>	
<b>U.S. PATENT DOCUMENTS</b>						
EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	INVENTOR NAME	CLASS/ SUBCLASS	FILING DATE	
<i>A.S.</i>	AA	5,103,394	Apr. 7, 1992	Blasciak	395/575 Dec. 21, 1989	
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DATE CONSIDERED <i>9/18/06</i>			EXAMINER <i>[Signature]</i>			
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APPLICANT <b>Dimpsey et al.</b>						
FILING DATE <b>March 22, 2004</b>				GROUP ART UNIT <b>2165-2185</b>		
<b>U.S. PATENT DOCUMENTS</b>						
EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	INVENTOR NAME		CLASS/ SUBCLASS	FILING DATE
<i>A.S.</i>	BB	2002/0129309	Sep. 12, 2002 Floyd et al.		714/724	Dec. 18, 2000
<i>I</i>	BC	2001/0032305	Oct. 18, 2001 Barry		712/34	Feb. 23, 2001
<i>I</i>	BD	2002/0199179	Dec. 26, 2002 Lavery et al.		717/158	Jun. 21, 2001
<i>I</i>	BE	2002/0124237	Sep. 5, 2002 Sprunt et al.		717/127	Dec. 29, 2000
<i>✓</i>	BF	2002/0147965	Oct. 10, 2002 Swaine et al.		717/124	Feb. 1, 2001
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<b>FOREIGN PATENT DOCUMENTS</b>						
EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	COUNTRY		CLASS/ SUBCLASS	TRANSLATION YES NO
<i>A.S.</i>	BH	JP2000029731	Dec. 8, 1999 Japan		G06F 9/38	<input type="checkbox"/> X
<i>A.S.</i>	BI	JP2000347863	Dec. 15, 2000 Japan		G06F 9/38	<input type="checkbox"/> X
<b>OTHER PRIOR ART (including author, title, date, pertinent page, etc.)</b>						
	BJ	<del>Kikuchi, "Parallelization Assist System", Joho Shori, Vol. 34, No. 9, Sept. 1993, pp. 1158-1169.</del>				
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<b>RELATED PATENT APPLICATIONS</b>						
EXAMINER INITIAL	APPLICATION NO/ ATTY. DOCKET NO.	APPLICANT	TITLE			FILING DATE
<i>A.S.</i>	BP 09/435,069 AT9-99-491	Davidson et al.	Method and Apparatus for Instruction Sampling for Performance Monitoring and Debug			Nov. 4, 1999
<i>A.S.</i>	BQ 08/538,071 AUT919950055US1	Gover et al.	Method and System for Selecting and Distinguishing an Event Sequence using an Effective Address in a Processing System			Oct. 2, 1995
DATE CONSIDERED <i>9/18/06</i>			EXAMINER <i>[Signature]</i>			
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APPLICANT <b>Dimpsey et al.</b>					
FILING DATE <b>March 22, 2004</b>				GROUP ART UNIT <b><del>2105</del> 2185</b>	
<b>RELATED PATENT APPLICATIONS</b>					
EXAMINER INITIAL	APPLICATION NO./ ATTY. DOCKET NO.	APPLICANT	TITLE	FILING DATE	
A.S.	BR 10/675,777 / AUS920030477US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Instruction Execution and Data Accesses	Sep. 30, 2003	
	BS 10/674,604 / AUS920030478US1	DeWitt, Jr. et al.	Method and Apparatus for Selectively Counting Instructions and Data Accesses	Sep. 30, 2003	
	BT 10/675,831 / AUS920030479US1	DeWitt, Jr. et al.	Method and Apparatus for Generating Interrupts Upon Execution of Marked Instructions and Upon Access to Marked Memory Locations	Sep. 30, 2003	
	BU 10/675,778 / AUS920030480US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Data Accesses and Instruction Executions that Exceed a Threshold	Sep. 30, 2003	
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	BW 10/675,751 / AUS920030482US1	DeWitt, Jr. et al.	Method and Apparatus for Debug Support for Individual Instructions and Memory Locations	Sep. 30, 2003	
	BX 10/675,721 / AUS920030483US1	Levine et al.	Method and Apparatus to Autonomically Select Instructions for Selective Counting	Sep. 30, 2003	
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	BZ 10/674,606 / AUS920030485US1	Levine et al.	Method and Apparatus to Autonomically Take an Execution on Specified Instructions	Sep. 30, 2003	
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	CB 10/675,872 / AUS920030487US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Instruction and Memory Location Ranges	Sep. 30, 2003	
	CC 10/757,250 / AUS920030488US1	Levine et al.	Method and Apparatus for Maintaining Performance Monitoring Structures in a Page Table for use in Monitoring Performance of a Computer Program	Jan. 14, 2004	
V	CD 10/757,192 / AUS920030543US1	DeWitt, Jr. et al.	Method and Apparatus for Providing Pre and Post Handlers for Recording Events	Jan. 14, 2004	
A.S.	CE 10/757,192 / AUS920030548US1	DeWitt, Jr. et al.	Method and Apparatus for Determining Computer Program Flows Autonomically Using Hardware Assisted Thread Stack Tracking and Cataloged Symbolic Data	March 18, 2004	
DATE CONSIDERED <b>9/18/06</b>			EXAMINER <i>[Signature]</i>		
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APPLICANT <b>Dimpsey et al.</b>					
FILING DATE <b>March 22, 2004</b>		GROUP ART UNIT <b>2165-2185</b>			
<b>RELATED PATENT APPLICATIONS</b>					
EXAMINER INITIAL	CLASS	APPLICATION NO / ATTY. DOCKET NO.	APPLICANT	TITLE	FILING DATE
A.S.	CF	10/757,227 / AUS920030555US1	DeWitt, Jr. et al.	Method and Apparatus for Autonomically Moving Cache Entries to Dedicated Storage when False Cache Line Sharing is Detected	Jan. 14, 2004
	CG	10/757,197 / AUS920030556US1	DeWitt, Jr. et al.	Method and Apparatus for Optimizing Code Execution Using Annotated Trace Information having Performance Indicator and Counter Information	Jan. 14, 2004
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	CI	10/808,716 / AUS920040060US1	Dimpsey et al.	Method and Apparatus for Autonomic Test Case Feedback Using Hardware Assistance for Code Coverage	Mar. 22, 2004
	CJ	10/806,576 / AUS920040061US1	Dimpsey et al.	Method and Apparatus for Hardware Assistance for Data Access Coverage	Mar. 22, 2004
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DATE CONSIDERED <b>9/18/06</b>			EXAMINER <i>[Signature]</i>		
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INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT

## Complete if Known

Application Number	10/806,871
Filing Date	03-22-2004
First Named Inventor	Dimpsey et al.
Examiner Name	Popovici, Dor Arsen Savk
Art Unit	2105 2185
Attorney Docket No.	AUS920040064US1

Examiner Initials	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
A.S.	AA 5,710,881	01-20-1998	Gupta et al.	
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Examiner Initial	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

## NON PATENT LITERATURE

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A.S.	AH	TANENBAUM, "Structured Computer Organization", 1984, Prentice-Hall, Inc., 2 <sup>nd</sup> Edition, pp. 10-12.		
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A.S.	AJ	ROTHMAN et al., "Analysis of Shared Memory Misses and Reference Patterns", 2000, IEEE, pp. 187-198.		

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# INFORMATION DISCLOSURE STATEMENT BY APPLICANT

## Complete if Known

Application Number	10/806,871
Filing Date	03-22-2004
First Named Inventor	Dimpsey et al.
Examiner Name	Popovich, Dev Arpin Savla
Art Unit	2185 2185
Attorney Docket No.	AUS920040064US1

Examiner Initials	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
A.S.	AA 5,797,019	08-18-1998	Levine et al.	
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Examiner Initial	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

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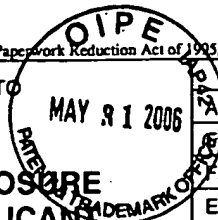
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A.S.	AG Wikipedia "JavaServer Pages" downloaded 1/24/06 <a href="http://en.wikipedia.org/wiki/JavaServer_Pages">http://en.wikipedia.org/wiki/JavaServer_Pages</a>
A.S.	AH "Hardware Cycle Based memory Residency", IBM, May 22, 2003, ip.com, IPCOM000012728D, pp. 1-2
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STATEMENT BY APPLICANT****Complete if Known**

Application Number	10/806,871
Filing Date	03-22-2004
First Named Inventor	Dimpsey et al.
Examiner Name	Popovici, Dov
Art Unit	2165
Attorney Docket No.	AUS920040064US1

Examiner Initials	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
AS	AA 5394529	02-28-1995	Brown, III et al.	
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Examiner Initial	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

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AS	AP JEONG et al., "Cost Sensitive Cache Replacement Algorithms", June 2002, Second Workshop on Caching, Coherence and Consistency, NY, pp. 1-14.

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Application Number	10806871
Filing Date	2004-03-22
First Named Inventor	Dimpsey et al.
Art Unit	2165-2185
Examiner Name	Gamm, Jeffrey A. Arpan Savla
Attorney Docket Number	AUS920040064US1

**U.S. PATENTS**

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Examiner Initial*	Cite No	Patent Number	Kind Code <sup>1</sup>	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
A.S.	1	6973417	B1	2005-12-06	Maxwell et al.	
A.S.	2	6721875	B1	2004-04-13	McCormick et al.	

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Application Number	10806871
Filing Date	2004-03-22
First Named Inventor	Dimpsey et al.
Art Unit	<del>2165</del> 2185
Examiner Name	<del>Gaffin, Jeffrey A.</del> Arpan Savla
Attorney Docket Number	AUS920040064US1

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Examiner Signature	<i>Arpan Savla</i>	Date Considered	9/18/06
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